

FORM PTO-1449 (MODIFIED)**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

Applicant(s): S.R. Cebenko et al.
 Case: 1-1-3-1-1
 Serial No.: TBA
 Filing Date: February 14, 2002
 Group: TBA

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U.S. PATENT DOCUMENTS

EXAMINER	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE	IF APPROPRIATE
<u>dhm</u>	09/400,029	9/21/99	J.A. Schadt, "Integrated Circuit With Standard Cell Logic and Spare Gates."			

FOREIGN PATENT DOCUMENTS

EXAMINER	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION
					YES NO

OTHER DOCUMENTS

EXAMINER	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

Examiner

Wae

Date Considered

03/30/04

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.